

E1.01 Radiation Tolerant GPU Architecture

Team Photon

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Background

This proof-of-concept GPU (POCGPU) architecture shows the viability of using readily available software and hardware components to develop GPU applications that have a potential path to a flight system. Since this is proof-of-concept, there will be no actual radiation testing but rather use the injection of errors. The POCGPU architecture should be scalable, allowing the architecture to be used for both low resolution and higher-resolution applications. The proof-of-concept architecture will be able to display graphics onto two different sized screens. It is anticipated that this work will lead to future work in GPU applications suitable for deep space missions.

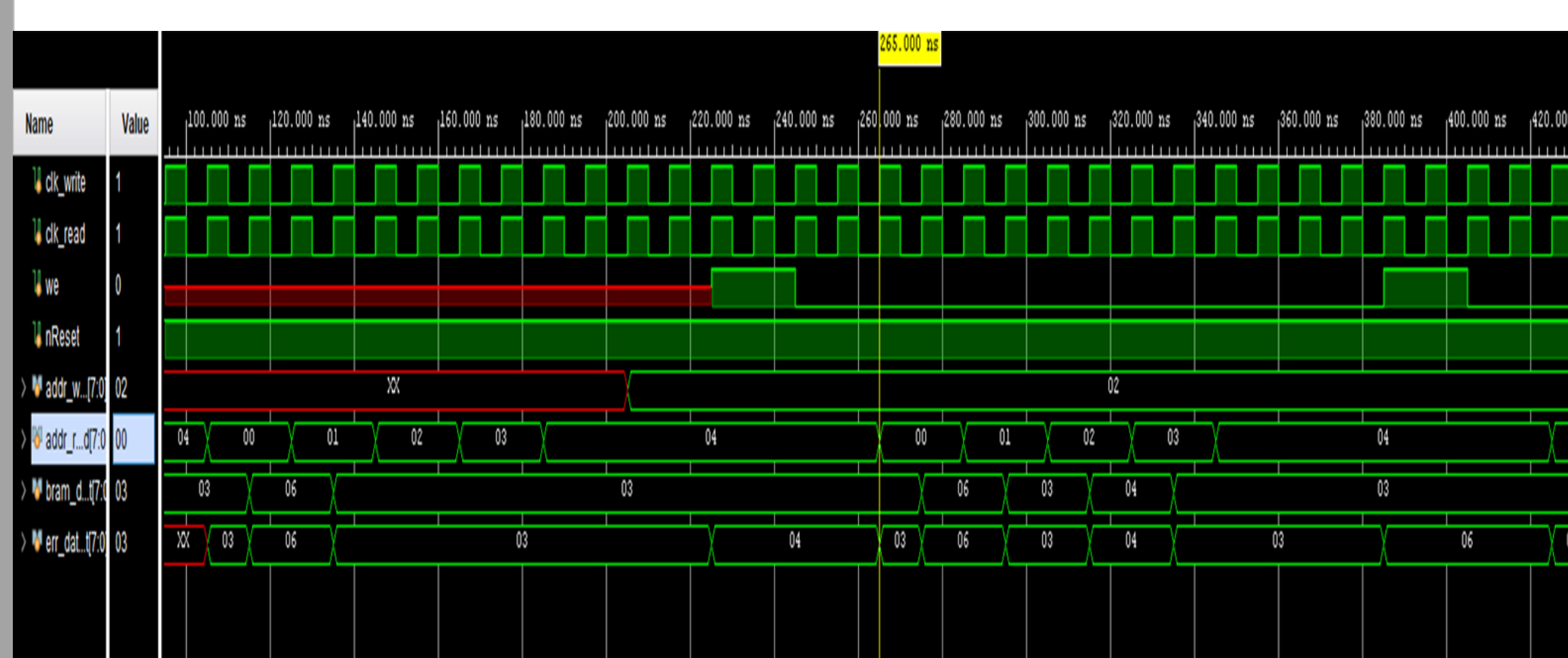
Design Requirements

- Scalable graphics outputs for different screen resolutions
- Use of commercial-off-the-shelf parts
- Utilizes open-source graphics data processing
- Error detection and correction algorithms embedded into the system
- Simulation of single-event-upsets/latch-ups due to ionizing radiation

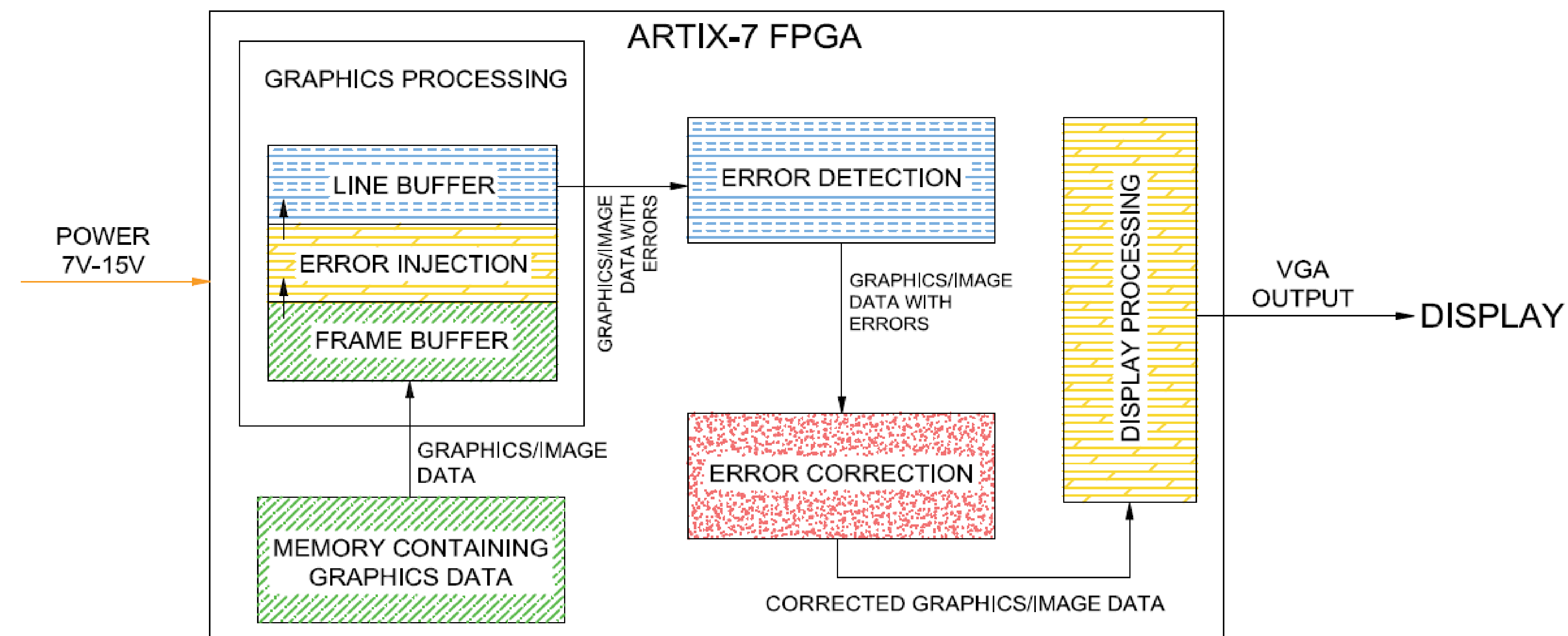
Future Plans

- Randomized error injection over summer
- Error detection and correction subroutines integrated into system by next semester
- Higher resolution output
- Path to space flight

Error Injection Testbench



System Block Diagram



SUBROUTINE:	KEY:	OWNER:
DATA IO/GRAPHICS PROCESSING		ERIC TEJEDA
ERROR DETECTION		PATRICK HORNBECK
ERROR INJECTION		RICHARD MEDINA
SYSTEM REPAIR/ERROR CORRECTION		CONNOR MARTIN

Feature	Description
Scalable	The system will have the ability to scale the output to different resolutions and screen sizes. Allowing for a wider range of uses.
Error Detection	The system will be able to determine when an error occurs. Errors will range from small upsets to catastrophic latch-ups.
Error Mitigation	Once an error is detected, the system will be able to correct the error and allow the graphics to continue to be displayed.
Self-Healing (stretch goal)	If a section of the graphics processing unit becomes unusable due to errors, the system will be able to replace the corrupted section with a backup copy of the section from memory.

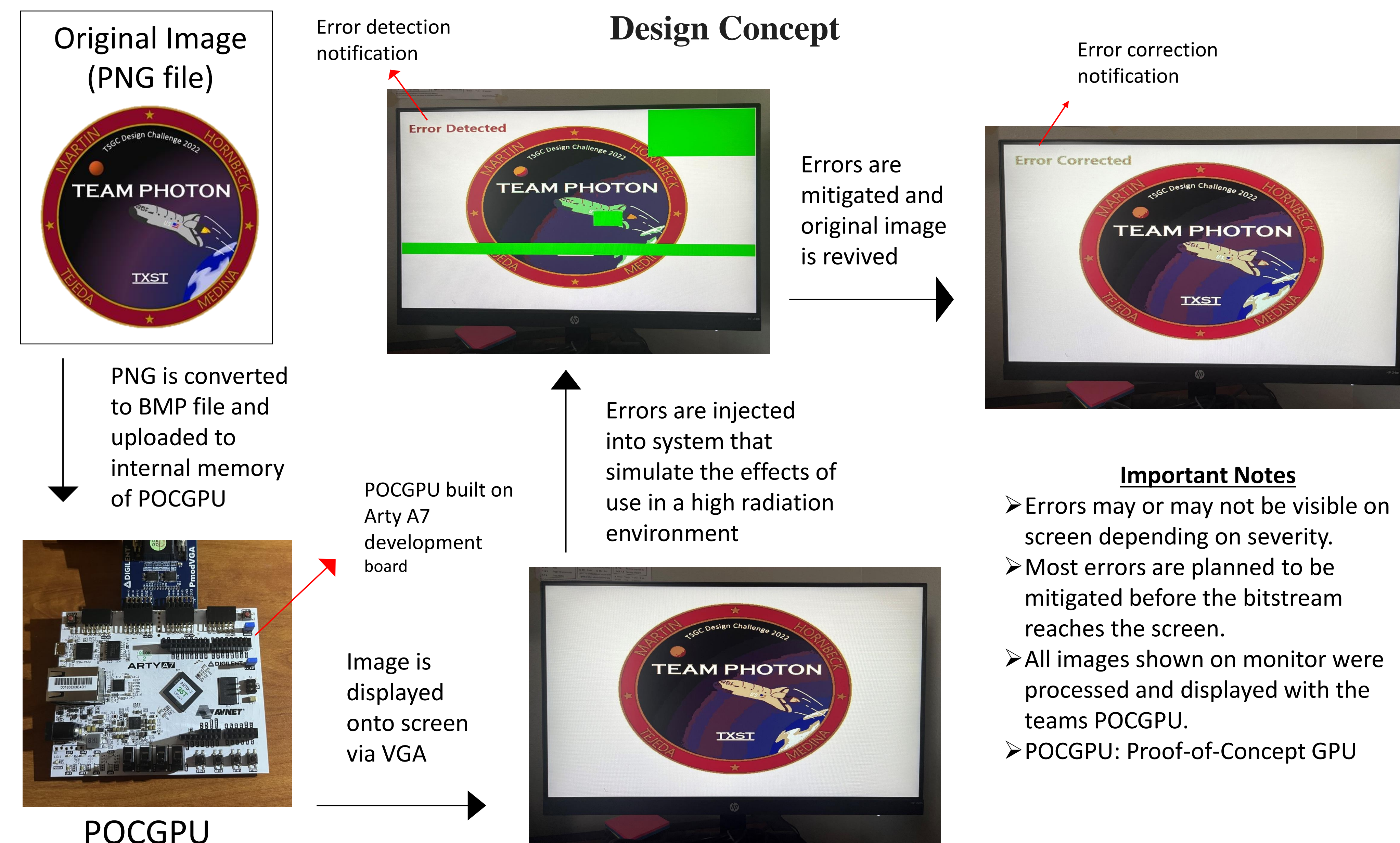
Design Risks

- Integration of graphics software with COTS hardware
- Memory allocation on device
- Simulating the effects of radiation on device

Design Considerations

- A controller that can process graphics while keeping the number of active components low, while keeping a high level of design freedom.
- Graphical outputs that represent the concepts we are attempting to prove that can be completed in a reasonable time frame.
- The development board should be able to port over open-source graphics processing software.
- Injected errors should simulate the harsh effects of use in a high radiation environment.

Design Concept



Acknowledgments

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Open-source code used: <https://projectf.io/>
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